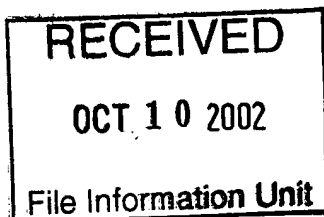


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REQUEST FOR ACCESS TO AN APPLICATION UNDER 37 CFR 1.14(e)



In re Application of	
Application Number 09/620,544	Filed 7/21-2000
Art Unit	Examiner

Paper No. 18Assistant Commissioner for Patents
Washington, DC 20231

1. ☐ I hereby request access under 37 CFR 1.14(e)(2) to the application file record of the above-identified ABANDONED Application, which is not within the file jacket of a pending Continued Prosecution Application (CPA) (37 CFR 1.53(d)) and is: (CHECK ONE)

☐ (A) referred to in:

United States Patent Application Publication No. _____, page _____, line _____,

United States Patent Number 5,845,313, column _____, line _____, or

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#18



US005845313A

United States Patent [19]
Estakhri et al.

[11] **Patent Number:** **5,845,313**
[45] **Date of Patent:** **Dec. 1, 1998**

- [54] **DIRECT LOGICAL BLOCK ADDRESSING FLASH MEMORY MASS STORAGE ARCHITECTURE**
- [75] Inventors: **Petro Estakhri**, Pleasanton; **Mahmud Assar**, Morgan Hill, both of Calif.
- [73] Assignee: **Lexar**, Fremont, Calif.
- [21] Appl. No.: **509,706**
- [22] Filed: **Jul. 31, 1995**
- [51] Int. Cl.⁶ **G06F 12/10**
- [52] U.S. Cl. **711/103; 711/104; 711/156; 711/203**
- [58] Field of Search **395/430, 431, 395/435, 483, 413; 711/103, 104, 108, 156, 203**

62-283497 12/1987 Japan .

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Primary Examiner—Eddie P. Chan
Assistant Examiner—Reginald G. Bragdon
Attorney, Agent, or Firm—Claude A. S. Hamrick; Oppenheimer W. Donnelly; Maryam Imam

[57] **ABSTRACT**

A nonvolatile semiconductor mass storage system and architecture can be substituted for a rotating hard disk. The system and architecture avoid erase cycles each time information stored in the mass storage is changed. Erase cycle are avoided by programming an altered data file into an empty mass storage block rather than over itself as a hard disk would. Periodically, the mass storage will need to be cleaned up. These advantages are achieved through the use of several flags, and a map to correlate a logical block address of a block to a physical address of that block. In particular, flags are provided for defective blocks, used blocks, and old versions of a block. An array of volatile memory is addressable according to the logical address and stores the physical address

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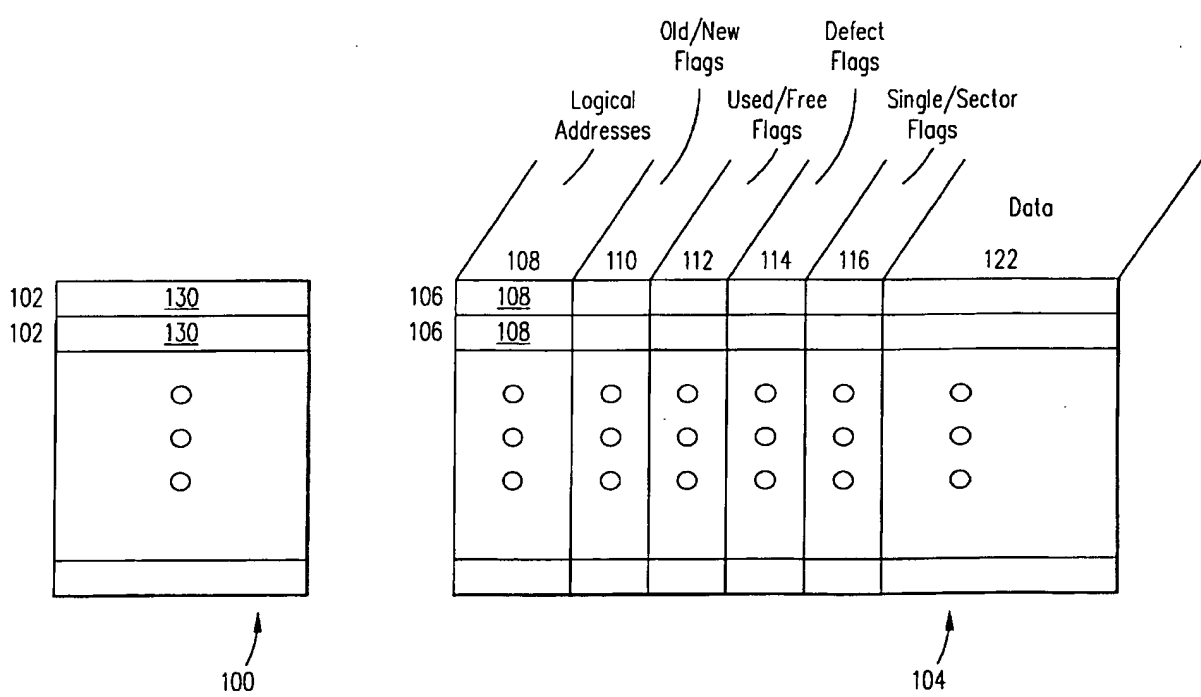
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8 Claims, 5 Drawing Sheets

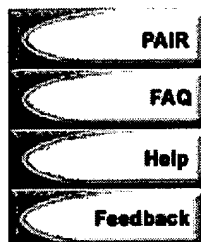




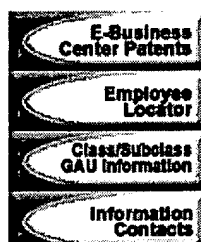
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